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Explain 9.2 The Overdamped Parallel RLC Circuit The  
circuit of Fig 9.2 is modified substantially, with the

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resistor being replaced with a k resistor, the inductor swapped out for a smaller mH version, the capacitor replaced with a nF alternative, and now the inductor is initially discharged while the capacitor is storing 7.2 mJ (a) Compute  $\alpha$ ,  $\omega_0$ ,  $s_1$ , and  $s_2$ , and verify that the circuit is

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